

(12) United States Patent

Baker et al.

US 7,213,323 B2 (10) Patent No.:

(45) Date of Patent: May 8, 2007

(54) METHOD OF FORMING AN ELECTRONIC PRESSURE SENSITIVE TRANSDUCER ON A PRINTED CIRCUIT BOARD

(75) Inventors: Jeffrey R. Baker, Thousand Oaks, CA (US); Carlos S. Sanchez, Oxnard, CA (US); Patrick H. Bair, Camarillo, CA

(US)

(73) Assignee: Interlink Electronics, Inc., Camarillo,

CA (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 22 days.

Appl. No.: 11/058,939

(22)Filed: Feb. 16, 2005

(65)**Prior Publication Data**

US 2005/0156705 A1 Jul. 21, 2005

Related U.S. Application Data

- (62) Division of application No. 10/067,952, filed on Feb. 5, 2002, now Pat. No. 6,909,354.
- Provisional application No. 60/267,455, filed on Feb. 8, 2001.
- (51) Int. Cl. G01R 3/00 (2006.01)
- **U.S. Cl.** **29/595**; 29/592.1; 29/831; 29/832; 29/841; 338/5; 338/47; 338/99; 338/114; 338/128; 438/48; 439/79
- Field of Classification Search 29/592.1, 29/595, 831, 832, 841; 338/5, 47, 99, 114, 338/128; 438/48; 439/79

See application file for complete search history.

(56)References Cited

U.S. PATENT DOCUMENTS

4,079,194	Α		3/1978	Kley	
4,085,302		*		•	 200/5 A
4,129,747	A		12/1978	Pepper, Jr.	
4,296,406	\mathbf{A}		10/1981	Pearson	
4,314,227	\mathbf{A}		2/1982	Eventoff	
4,314,228	Α		2/1982	Eventoff	
4,349,712	Α		9/1982	Michalski	
4,475,008	Α		10/1984	Doi et al.	
4,489,302	Α		12/1984	Eventoff	
4,598,181	Α		7/1986	Selby	
4,620,062	Α		10/1986	Mizzi et al.	
4,623,757	\mathbf{A}		11/1986	Marino	

(Continued)

FOREIGN PATENT DOCUMENTS

JP	52-56957	5/1977
WO	WO 02/057731 A1	7/2002

Primary Examiner—Paul D. Kim (74) Attorney, Agent, or Firm—Brooks Kushman P.C.

(57)ABSTRACT

The cost and complexity of an electronic pressure sensitive transducer are decreased by constructing such a transducer directly on a printed circuit board containing support electronics. Conductive traces are formed on the printed circuit board to define a contact area. A flexible substrate having an inner surface is positioned over the contact area. An adhesive spacer, substantially surrounding the contact area, attaches the flexible substrate to the printed circuit board. At least one resistive layer is deposited on the flexible substrate inner surface. In use, the resistive layer contacts at least two conductive traces in response to pressure applied to the flexible substrate to produce an electrical signal indicative of applied pressure.

7 Claims, 5 Drawing Sheets

